

MAR-09-06 THU 09:48 AM

FAX NO.

RECEIVED  
CENTRAL FAX CENTER

P. 01

MAR 09 2006

**CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)**

Applicant(s): Bernstein et al.

Docket No.

BUR920040024US1

Application No. 10/710,453	Filing Date 7/13/2004	Examiner Vibol Tan	Group Art Unit 2819
-------------------------------	--------------------------	-----------------------	------------------------

Invention: **LOW LEAKAGE MONOTONIC CMOS LOGIC**

I hereby certify that this Request for Reconsideration (21 pages)  
*(Identify type of correspondence)*

is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. 571-273-8300)

on 3/9/06  
*(Date)*

**Kim Dwileski**

*(Typed or Printed Name of Person Signing Certificate)*

*Kim Dwileski*  
*(Signature)*

**Note: Each paper must have its own certificate of mailing.**

P18/REV02

MAR-09-06 THU 09:49 AM

FAX NO.

P. 02

**RECEIVED  
CENTRAL FAX CENTER**

**MAR 09 2006**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Appl. No.: 10/710,453

Applicant(s): Berstein et al.

Filed.: July 13, 2004

Art Unit: 2819

Dkt. No.: BUR920040024US1

Examiner: Vibol Tan

Title: **LOW LEAKAGE MONOTONIC CMOS LOGIC**

---

Honorable Commissioner for Patents

P. O. Box 1450  
Alexandria, VA 22313-1450

**REQUEST FOR RECONSIDERATION**

This Request for Reconsideration is being filed in response to the Office Action mailed on December 20, 2005.

BUR920040024US1

1